REMARKS

Reconsideration of this application is respectfully requested in view of the foregoing amendment and the following remarks.

Claims 14, 15, 17-21, 23-26, and 28-37 remain pending in this application. Independent claims 14, 20, 25 and 32 have been amended hereby. Support for the amendments to the claims can be found in, e.g., Figures 1 and 3 and pages 5 and 7 of the application. No new matter has been presented. For the reasons set forth below, all of the claims presently pending in this application are believed to be in condition for allowance.

In the Office Action dated March 2, 2010,

- Claims 14, 15, 17-21, 23-26, 28, 29 and 31-37 were rejected under 35 U.S.C. §103(a) as being unpatentable over Minnis et al. (US 6,954,628; "Minnis"); and
- Claim 30 was rejected under 35 U.S.C. §103(a) as being unpatentable over Minnis in view of Reisch et al. (US 5,168,375; "Reisch").

These grounds of rejection are respectfully traversed.

The present invention provides a mixed signal chip 10 to process received radio signals of a given one of two receiver systems (e.g., one in accordance with UMTS (3G) and another in accordance with GSM (2G)). Based on the given receiver system, the invention selects an appropriate configuration for various components, including, for example, an ADC, Decimator, FIR filter, and Sample Rate Adaption unit. See Figures 1-3 of the present application.

As set forth above, each of the independent claims has been amended to recite additional features with respect to the treatment of GSM (2G) signals. As now required by the claims, the sample rate of the GSM signal is altered after filtering (e.g., via filter 24, Figure 1) by employing an interpolator followed by a decimator. As explained at the bottom of page 5 of the instant application, interpolator 32 adds samples into the digital signal travelling on path 26 and decimator 34 removes samples from the signals travelling on path 26.

In one implementation, as explained at the bottom of page 7 of the application, the output of filter 24 is first interpolated to increase the sample rate by a factor of 65 and is then decimated

to reduce the sample rate by a factor of 192. "The net effect of the adaptation unit 30 is to change the sample rate of the output of the filter 24 by a factor of 65/192" to provide an ultimate sample rate of 270.83 ksps.

It is respectfully submitted that neither Minnis nor Reisch discloses or suggests the combination of an interpolator and decimator to obtain a desired sample rate, as is required by the amended claims. As such, Applicant respectfully requests that the §103(a) rejections asserted against the claims be reconsidered and withdrawn.

In view of the foregoing all of the claims in this case are believed to be in condition for allowance. Should the Examiner have any questions or determine that any further action is desirable to place this application in even better condition for issue, the Examiner is encouraged to telephone applicants' undersigned representative at the number listed below.

Dated: May 27, 2010

Respectfully submitted by:

/Lawrence D. Eisen/

Lawrence D. Eisen
Reg. No. 41009

EDELL, SHAPIRO & FINNAN, LLC
CUSTOMER NO. 27896
1901 Research Boulevard, Suite 400
Rockville, MD 20850
(301) 424-3640